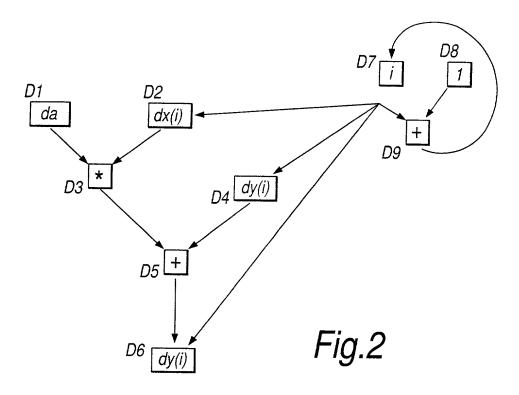


Fig.1



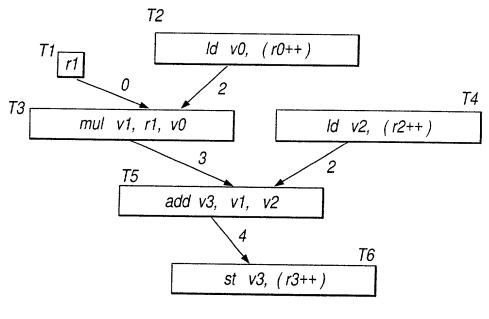


Fig.3

Issue slot 2 Issue slot 3
mul v1, v0, r1
add v3, v1, v2
st v3, (r3++)

Fig.4

Fig.5

	-		140	O doitor				lte.	Iteration 1		<u></u>
	_		911	Herallon o				-			
Cycle	Offset	Offset Instruction	0/	VI	1/2	х3	Instruction	8	11	7/2	V3
ì	9	T ₊	r4 (* p10)								
1	9	7	r4 (* p10)								
2	5	mul v1, v0, r1 r5 (* p10)	r5 (> p10)	r6 (* p11)			ld v0, (r0 ++)	r4 (* p9)			
3	5	1d v2, (r2++)	1	r6 (* p11)	16 (* p11) 18 (* p13)			r4 (* p9)			
-	4			r7 (* p11)	r7 (* p11) r9 (* p13)		mul v1, v0, r1 r5 (* p9) r6 (* p10)	r5 (* p9)	r6 (* p10)		
- 4	.	CV LV SV PDC		17 (* 011)	r9 (* p13)	r10 (* p14)	17 (* D11) 19 (* D13) 110 (* D14) 10 V2, (12++)		r6 (* p10) r8 (* p12)	r8 (* p12)	
	+ 0	auu vo, v 1, ve				r11 (* 014)			r7 (* p10)	r7 (* p10) r9 (* p12)	
ا ۵	2 6					r11 (> n14)	11 (14) add v3 v1 v2		r7 (* p10)	19 (* p12)	r7 (+ p10) r9 (+ p12) r10 (+ p13)
7	3					7	2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,				111/2 013)
8	2					r12 (* p14)					(0.2
0	0	ct 1/3 (r3++)				r12 (> p14)					r11 (* p13)
,	<u>, </u>	31 10, (10.1)									r12 (* p13)
22	-		 	1			11.07				(12 (* 013)
11	_	المسالح الماد					St V3, (r3++)				

Fig.6A

Ref. No

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								<i></i>	/ I	<u> </u>								
	6/1	3											r10 / 011)	10 (2 0) 110 (2 0)	(11d d) 111	(11 (* p11)	r12 (> p11)	r12 (* p11)
	2	7,									r8 (* n10)			010 -101				
Iteration 3	7									r6 (▼ n8)	(\$u €)	r7 (* n8)	r7 (* n8)	(nd -) ::				
	0/1	2						r4 (* D7)	14 (> 07)	15 (* 07)	-							
	Instruction							ld v0, (r0 ++)		mul v1. v0. r1 r5 (* D7) r6 (* D8)	19 (* p11) (10 (* n12) Id v2 (12++)	(11 (* 012) add v3 v1 v2	dd 10, 11, 12				st v3, (r3++)
	1/3										r10 (> n12)	r11 (> 012)	(11 (- 012)	r12 (* n12)	r12 (= p12)	17 (DIC)		
	1/2								r6 (* p9) r8 (* p11)	r7 (* p9) r9 (* p11)	r9 (+ p11)							
Iteration 2	11							(6d ◆) 9ı	r6 (► p9)	r7 (* p9)	17 (* p9)							
1fe	0/					r4 (* p8)	r4 (* p8)	r5 (► p8)										
	Instruction					ld v0, (r0++)		mul v1, v0, r1	ld v2, (r2++)		add v3, v1, v2				St v3. (r3++)	/ 2.1 /		
	Offset	9	9	5	5	4	4	3	3	2	2	+	1	0	0	-		+-
	Cycle	0	1	2	3	4	5	9	7	8	9	10	11	12	13	17	F !	15

Fig.6B

		Iteration 0	Iteration 1	Iteration 2	Iteration 3
Cycle	Offset	Instruction	Instruction	Instruction	Instruction
0	6	ld r4, (r0++)			
1	6				
2	5	mul r6, r5, r1	ld r4, (r0++)		
3	5	ld r8, (r2++)			
4	4		mul r6, r5, r1	ld r4, (r0++)	
5	4	add r10, r7, r9	ld r8, (r2++)		
6	3			mul r6, r5, r1	ld r4, (r0++)
7	3		add r10, r7, r9	ld r8, (r2++)	
8	2				mul r6, r5, r1
9	2	st r14, (r3++)		add r10, r7, r9	ld r8, (r2++)
10	1				
11	1		st r12, (r3++)		add r10, r7, r9
12	0				
13	0			st r12, (r3++)	
14	-1				
15	-1				st r12, (r3++)

Fig.7

		<u></u>	<u> </u>		
		Iteration 0	Iteration 1	Iteration 2	Iteration 3
Cycle	Offset	Instruction	Instruction	Instruction	Instruction
0	6	ld p10, (p0++)			
1	6				
2	5	mul p11, p10, p1	ld p9, (p0++)		
3	5	ld p13, (p2++)			
4	4		mul p10, p9, p1	ld p8, (p0++)	
5	4	add p14, p11, p13	ld p12, (p2++)		
6	3			mul p9, p8, p1	Id p7, (p0++)
7	3		add p13, p10, p12	ld p11, (p2++)	
8	2				mul p8, p7, p1
9	2	st p14, (p3++)		add p12, p9, p11	ld p10, (p2++)
10	1				
11	1		st p13, (p3++)		add p11, p8, p10
12	0				
13	0			st p12, (p3++)	
14	-1				
15	-1				st p11, (p3++)

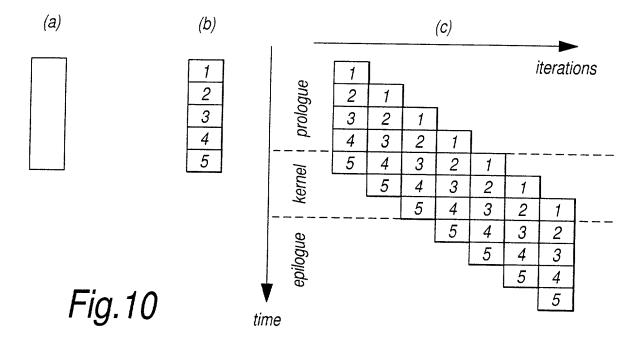
Fig.8

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				T		T				_	Τ	1	T					T	$\overline{}$
	Stage	Siage			7		7	20	2	B	c	7	33		c				
C tolo ollo	Issue siou 3	ייכומווסוו		C	5	+	_	n ū	2	-	3	2 0	7		3				
	Instruction			אין אין	11,0,10,11	1, 7, 2, 1, m	110,10,111	400 110, 11, 19	mul r6, r5, r1	add r10, r7, r9	mil r. r.s r.1	0 4 40 11 0	add 110, 17, 19		add r10 r7 ra	0, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,			
	Stage											7	ار		2		5	5	7
Issue slot 2	Iteration											0	,		-		2		2
58/	Instruction											st r12. (r3++)			st r12, (r3++)		st r12. (r3++)	, , ,	st r12 (r3++)
	Stage	1		1	2	1	2	1		7		2							
Issue slot 1	Iteration	0		1	0	2	1	3	, ,	7		3							
ssl	Instruction	ld r4, (r0++)		ld r4, (r0++)	ld r8, (r2++)	ld r4, (r0++)	ld r8, (r2++)	ld r4, (r0++)	(1,C1) 81 P	1010, (12++)		ld r8, (r2++)							
	Cycle	0	1	2	3	4	5	9	7		œ	6	10	1.4		12	13	14	15

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	·	,		·				 P5	P4	P3	P2	P1
Ф	1							0	0	0	0	1
ngo	2	1						0	0	0	1	1
prologue	3	2	1					0	0	1	1	1
	4	3	2	1				 0	1	1	1	1
<u> </u>	5	4	3	2	1			1	1	1	1	1
kernel		5	4	3	2	1		1	1	1	1	1
~			5	4	3	2	1	1	1	1	1	1
0)				5	4	3	2	1	1	1	1	0
ngc					5	4	3	1	1	1	0	0
epilogue						5	4	1	1	0	0	0
[5	1	0	0	0	0

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Fig.11

time

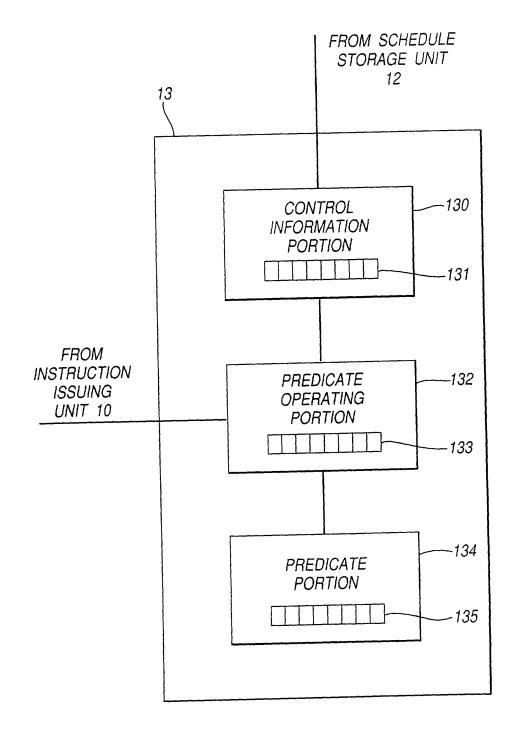
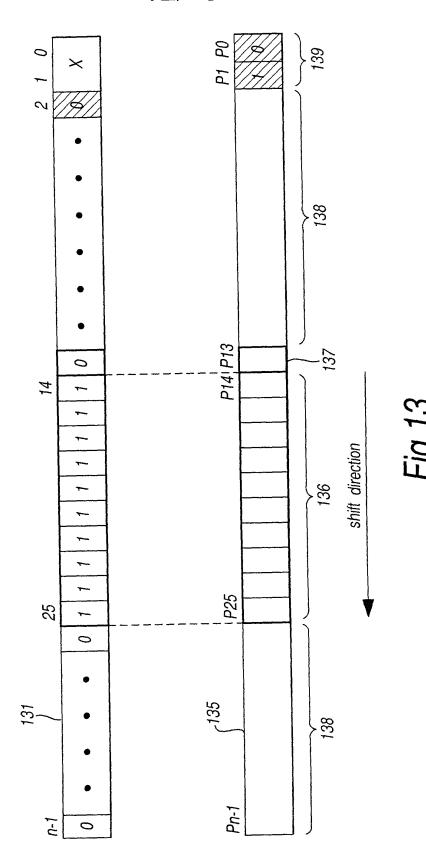
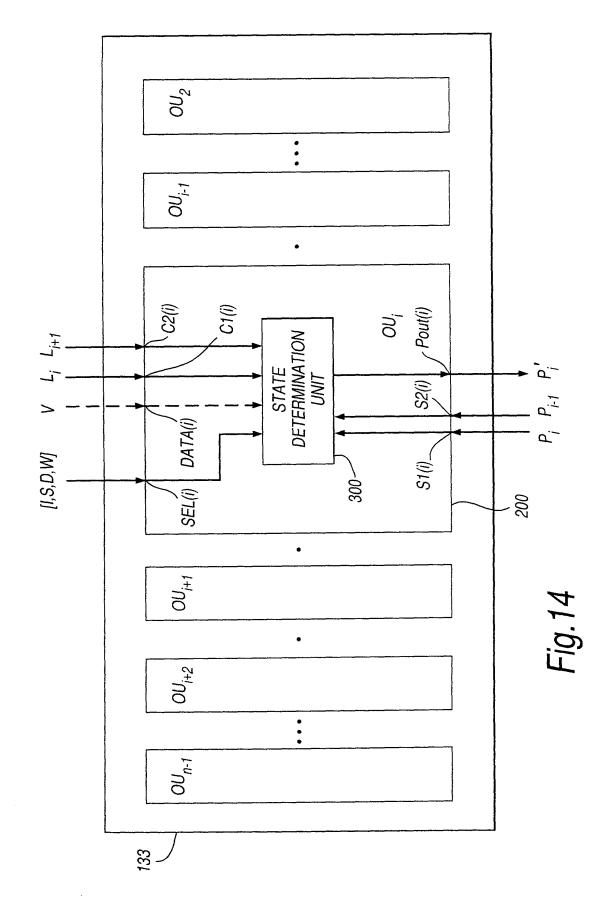
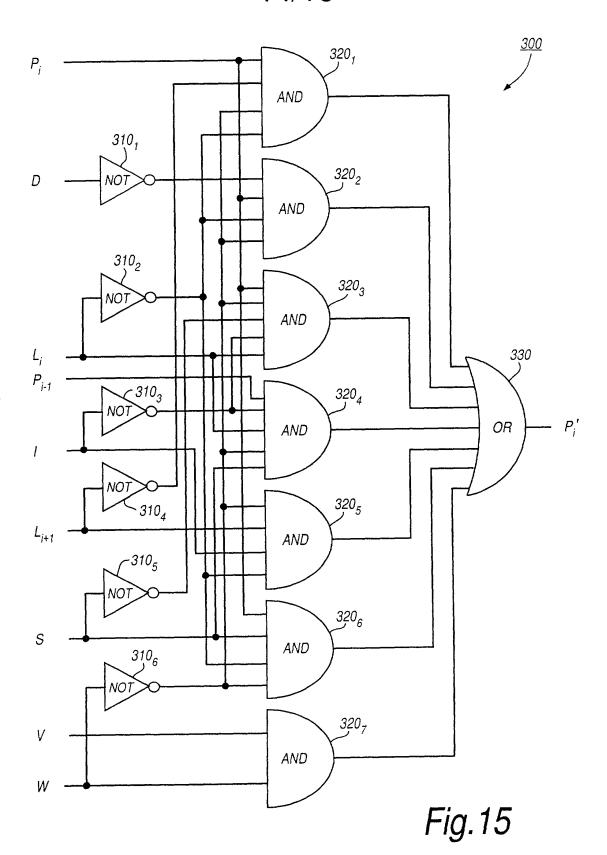


Fig.12

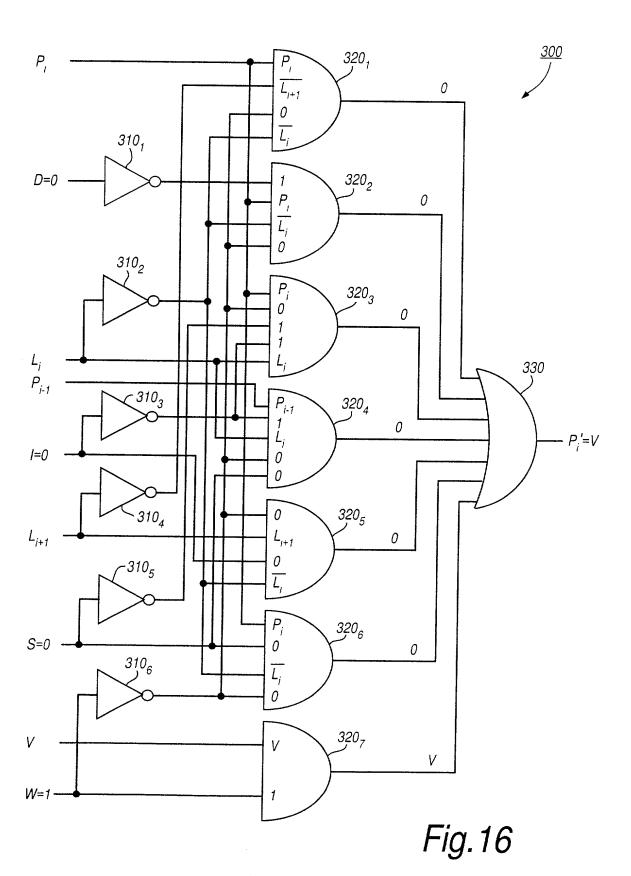


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